L	Hits	Search Text	DB	Time stamp
Number	uics	Search leve	76	TIME Scamp
6	28	(("5812416") or ("5774380") or	USPAT;	2004/03/25
		("5666289") or ("5661662") or ("5644498") or ("5638380") or ("5617431") or	US-PGPUB; EPO; JPO;	08:09
		("5550839") or ("5526277") or ("5487018")	DERWENT	
		or ("5452227") or ("5424589") or		
_	_	("5384710") or ("5313615")).PN.		
7	2	"6425109"	USPAT; US-PGPUB;	2004/03/25 08:11
			EPO; JPO;	00.11
			DERWENT	
10	30	((("5812416") or ("5774380") or	USPAT;	2004/03/25
		("5666289") or ("5661662") or ("5644498") or ("5638380") or ("5617431") or	US-PGPUB; EPO; JPO;	08:11
		("5550839") or ("5526277") or ("5487018")	DERWENT	
		or ("5452227") or ("5424589") or		
		("5384710") or ("5313615")).PN.) or		
11	2	"6425109" ((binary adj1 decision adj1 diagram) or	USPAT;	2004/03/25
**		BDD or (binary adj2 diagram) or OBDD or	US-PGPUB;	10:35
		Boolean) and (((("5812416") or	EPO; JPO;	
	,	("5774380") or ("5666289") or ("5661662") or ("5644498") or ("5638380") or	DERWENT	
		("5617431") or ("5550839") or ("5526277")		
		or ("5487018") or ("5452227") or		
	•	("5424589") or ("5384710") or		
19	199	("5313615")).PN.) or "6425109") (core same pin) and (classif\$7 or	USPAT;	2004/03/25
10	133	arrang\$5) and ((binary adj2 diagram) or	US-PGPUB;	09:06
	:	boolean)	EPO; JPO;	
20	22	(core same pin) and classif\$7 and	DERWENT USPAT;	2004/03/25
20	22	((binary adj2 diagram) or boolean)	US-PGPUB;	09:14
			EPO; JPO;	. = -
01		/mana nama alamai 507\ and 401- and	DERWENT	2004/02/25
21	/	(core same classif\$7) and (pin same classif\$7) and ((binary adj2 diagram) or	USPAT; US-PGPUB;	2004/03/25
		boolean)	EPO; JPO;	
		, , , , , , , , , , , , , , , , , , , ,	DERWENT	0004/00/05
22	13	(core same classif\$7) and (node same classif\$7) and ((binary adj2 diagram) or	USPAT; US-PGPUB;	2004/03/25
		boolean)	EPO; JPO;	55.30
		,	DERWENT	
23	13		USPAT;	2004/03/25
		classif\$7) and ((binary adj2 diagram) or boolean or BDD or OBDD or BED)	US-PGPUB; EPO; JPO;	09:33
			DERWENT	
24	6465	core and pin and ((binary adj2 diagram)	USPAT;	2004/03/25
		or boolean or BDD or OBDD or BED)	US-PGPUB; EPO; JPO;	09:35
			DERWENT	
25	2206		USPAT;	2004/03/25
		order\$3 or sort\$3 or grad\$3 or arrang\$3)	US-PGPUB; EPO; JPO;	09:38
		same (core or pin) and (core and pin and (binary adj2 diagram) or boolean or BDD	DERWENT	
		or OBDD or BED))		
26	367		USPAT;	2004/03/25
	İ	order\$3 or sort\$3 or grad\$3 or arrang\$3) near2 (core or pin) and (core and pin and	US-PGPUB; EPO; JPO;	10:20
		((binary adj2 diagram) or boolean or BDD	DERWENT	
		or OBDD or BED))		
27	55	_ ·	USPAT;	2004/03/25
		((categoriz\$5 or classif\$7 or organiz\$3 or order\$3 or sort\$3 or grad\$3 or	US-PGPUB; EPO; JPO;	10:03
		arrang\$3) near2 (core or pin) and (core	DERWENT	
		and pin and ((binary adj2 diagram) or		
		boolean or BDD or OBDD or BED)))		

	,			T-2
28	122	match\$3 and ((core or block or cell or macrocell or cluster or unit or subcircuit or subset or sub-circuit or sub-set) same pin) and ((categoriz\$5 or classif\$7 or organiz\$3 or order\$3 or sort\$3 or grad\$3 or arrang\$3) near2 (core or pin) and (core and pin and ((binary adj2 diagram) or boolean or BDD or OBDD or BED)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25
29	6948	716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:08
30	8	(match\$3 and ((core or block or cell or macrocell or cluster or unit or subcircuit or subset or sub-circuit or sub-set) same pin) and ((categoriz\$5 or classif\$7 or organiz\$3 or order\$3 or sort\$3 or grad\$3 or arrang\$3) near2 (core or pin) and (core and pin and ((binary adj2 diagram) or boolean or BDD or OBDD or BED)))) and 716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:19
31	28		USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:20
32	6		USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:20
33	34	((("5870308") or ("5689432") or ("5541849") or ("5537328") or ("5502649") or ("5483461") or ("5418733") or ("5378904") or ("5349542") or ("5313079") or ("5047949") or ("4731643") or ("4638442") or ("4612618")).PN.) or "6083271"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25
34	4	(categoriz\$5 or classif\$7 or organiz\$3 or order\$3 or sort\$3 or grad\$3 or arrang\$3) same (core or pin) and ((("5870308") or ("5689432") or ("5541849") or ("5537328") or ("5502649") or ("5483461") or ("5418733") or ("5378904") or ("5349542") or ("5313079") or ("5047949") or ("4731643") or ("4638442") or ("4612618")).PN.) or "6083271")	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25
35	3	(BDD or (binary adj2 diagram) or OBDD or BED or Boolean) and ((categoriz\$5 or classif\$7 or organiz\$3 or order\$3 or sort\$3 or grad\$3 or arrang\$3) same (core or pin) and ((("5870308") or ("5689432") or ("5541849") or ("5537328") or ("5502649") or ("5483461") or ("5418733") or ("5378904") or ("5349542") or ("5313079") or ("5047949") or ("4731643") or ("4638442") or ("4612618")).PN.) or "6083271"))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25
36	20	"4638442"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:38

				r
37	1	((BDD or (binary adj2 diagram) or OBDD or	USPAT;	2004/03/25
]		BED or Boolean) and ((categoriz\$5 or	US-PGPUB;	10:47
		classif\$7 or organiz\$3 or order\$3 or	EPO; JPO;	
		sort\$3 or grad\$3 or arrang\$3) same (core	DERWENT	
		or pin) and (((("5870308") or ("5689432")		
		or ("5541849") or ("5537328") or		
		("5502649") or ("5483461") or ("5418733")	İ	
		or ("5378904") or ("5349542") or		1
		("5313079") or ("5047949") or ("4731643")		
	Į	or ("4638442") or ("4612618")).PN.) or		
		"6083271"))) and "4638442"		
38	8		USPAT;	2004/03/25
			US-PGPUB;	10:50
			EPO; JPO;	
			DERWENT	
39	5	pin and (variable and "4638442")	USPAT;	2004/03/25
		,	US-PGPUB;	10:51
			EPO; JPO;	10001
			DERWENT	
I -	2	("6477691").PN.	USPAT;	2004/03/23
	"	1	US-PGPUB;	14:25
			EPO; JPO;	
			DERWENT].
1 -	2	("6324680").PN.	USPAT;	2004/03/23
		1 000.000 /	US-PGPUB;	14:29
			EPO; JPO;	14.29
			DERWENT	
l _	2	("6163876").PN.	USPAT;	2004/03/23
		(01030/0).PN.	•	14:31
			US-PGPUB;	14:31
			EPO; JPO;	
	2	(UE740647U) DV	DERWENT	2004/02/22
-	4	("5748647").PN.	USPAT;	2004/03/23
	ļ		US-PGPUB;	14:33
			EPO; JPO;	
		//UC2201E0U) /UE070210U)	DERWENT	0004/00/00
-	4	(("6338158") or ("5870310")).PN.	USPAT;	2004/03/23
			US-PGPUB;	14:33
			EPO; JPO;	,
	2	"6477691"	DERWENT	2004/02/22
-		0477691"	USPAT;	2004/03/23
	1		US-PGPUB;	14:34
			EPO; JPO; DERWENT	
_		/////6220150//\ 05 ///5070210//\ DV \		2004/02/22
1	6	((("6338158") or ("5870310")).PN.) or	USPAT;	2004/03/23
	[04//031	US-PGPUB;	14:35
			EPO; JPO;	
l _	2	//gratem add1 on add abda1 au gog1	DERWENT	2004/03/04
_		'	USPAT;	2004/03/24
		pin and (((("6338158") or	US-PGPUB;	09:40
		("5870310")).PN.) or "6477691")	EPO; JPO;	
		nin and //////C220150#\	DERWENT	2004/02/22
-	4	pin and (((("6338158") or	USPAT;	2004/03/23
		("5870310")).PN.) or "6477691")	US-PGPUB;	14:42
		·	EPO; JPO;	
_	410	//avatam adil am adil abis as 200)	DERWENT	1 2004/02/22
_	419	3	USPAT;	2004/03/23
		pin	US-PGPUB;	15:04
			EPO; JPO;	
	11100	//	DERWENT	
~	11129	,, 2	USPAT;	2004/03/23
		pin	US-PGPUB;	14:47
			EPO; JPO;	
		//	DERWENT	0004/05/55
-	3347	' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' ' '	USPAT;	2004/03/23
		(((system adj1 on adj1 chip) or SOC) and	US-PGPUB;	15:05
		pin)	EPO; JPO;	
L	L		DERWENT	

-	1	((assess\$3 or calculat\$3 or compute or	USPAT;	2004/03/23
		computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) with	US-PGPUB; EPO; JPO;	16:46
		(compatib\$5 or consisten\$ or agreeing or	DERWENT	
		conforming or accordans or congruous or	DEKWENT	
		harmonious or cooperat\$4 or ageeable or		
		consonan\$2 or corresponden\$2) with pin)		
		and (((core or block or cell) same pin)		
		and (((system adj1 on adj1 chip) or SOC)		
		and pin))		
-	84	((compatib\$5 or consisten\$ or agreeing or	USPAT;	2004/03/23
		conforming or accordan\$ or congruous or	US-PGPUB;	15:07
1		harmonious or cooperat\$4 or ageeable or	EPO; JPO;	
		consonan\$2 or corresponden\$2) with pin)	DERWENT	
1	i	and (((core or block or cell) same pin)		
		and (((system adj1 on adj1 chip) or SOC)		
	0	and pin))	USPAT;	2004/03/24
-	U	((binary adj1 decision adj1 diagram) or BDD) and (((compatib\$5 or consisten\$ or	US-PGPUB;	08:22
		agreeing or conforming or accordan\$ or	EPO; JPO;	00.22
		congruous or harmonious or cooperat\$4 or	DERWENT	
		ageeable or consonan\$2 or corresponden\$2)		
		with pin) and (((core or block or cell)		
		same pin) and (((system adj1 on adj1		
		chip) or SOC) and pin)))		
-	0	((binary near3 diagram) or (decision	USPAT;	2004/03/24
		near3 diagram) or BDD) and (((compatib\$5	US-PGPUB;	08:22
		or consisten\$ or agreeing or conforming	EPO; JPO;	
		or accordan\$ or congruous or harmonious	DERWENT	
		or cooperat\$4 or ageeable or consonan\$2		
		or corresponden\$2) with pin) and (((core		
		or block or cell) same pin) and (((system adj1 on adj1 chip) or SOC) and pin)))		
_	2423	((system adj1 on adj1 chip) or SOC) same	USPAT;	2004/03/23
	2123	(pin or output or input or I/O or	US-PGPUB;	16:34
		input/output)	EPO; JPO;	
			DERWENT	
-	1299	((core or block or cell) same (pin or	USPAT;	2004/03/23
		output or input or I/O or input/output))	US-PGPUB;	15:31
		and (((system adj1 on adj1 chip) or SOC)	EPO; JPO;	
		same (pin or output or input or I/O or	DERWENT	
	1	input/output))	IICDAM -	2004/03/23
-	1	((assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or	USPAT; US-PGPUB;	15:06
		reckon\$4 or apprais\$3 or judg\$3) with	EPO; JPO;	13.00
		(compatib\$5 or consisten\$ or agreeing or	DERWENT	
		conforming or accordan\$ or congruous or		
		harmonious or cooperat\$4 or ageeable or		
		consonan\$2 or corresponden\$2) with pin)		
		and (((core or block or cell) same (pin		
		or output or input or I/O or		
]		input/output)) and (((system adj1 on adj1		
		chip) or SOC) same (pin or output or		
† _	136	input or I/O or input/output)))	IICDAM.	2004/02/22
-	136	((compatib\$5 or consisten\$ or agreeing or	USPAT; US-PGPUB;	2004/03/23 15:33
		conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or	EPO; JPO;	13.33
		consonan\$2 or corresponden\$2) with (pin	DERWENT	
		or output or input or I/O or		
		input/output)) and (((core or block or		
		cell) same (pin or output or input or I/O		
		or input/output)) and (((system adj1 on		
		adj1 chip) or SOC) same (pin or output or		
1 1		<pre>input or I/O or input/output)))</pre>		
		adj1 chip) or SOC) same (pin or output or		

-	0	((binary adjl decision adjl diagram) or	USPAT;	2004/03/24
1		BDD) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or	US-PGPUB; EPO; JPO;	09:30
		congruous or harmonious or cooperat\$4 or	DERWENT	
		ageeable or consonan\$2 or corresponden\$2)		
		with (pin or output or input or I/O or		
		input/output)) and (((core or block or		
}		cell) same (pin or output or input or I/O		
		or input/output)) and (((system adj1 on adj1 chip) or SOC) same (pin or output or		
		input or I/O or input/output))))		
-	0	((binary near3 diagram) or (decision	USPAT;	2004/03/23
		near3 diagram) or BDD) and (((compatib\$5	US-PGPUB;	15:08
		or consisten\$ or agreeing or conforming	EPO; JPO;	
		or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2	DERWENT	
		or corresponden\$2) with (pin or output or		
		input or I/O or input/output)) and		,
		(((core or block or cell) same (pin or		
		output or input or I/O or input/output))		
		and (((system adj1 on adj1 chip) or SOC) same (pin or output or input or I/O or		
		input/output))))		
-	19	boolean and (((compatib\$5 or consisten\$	USPAT;	2004/03/23
1		or agreeing or conforming or accordan\$ or	US-PGPUB;	15:09
		congruous or harmonious or cooperat\$4 or	EPO; JPO;	
1		ageeable or consonan\$2 or corresponden\$2) with (pin or output or input or I/O or	DERWENT	
		input/output)) and (((core or block or		
		cell) same (pin or output or input or I/O		
		or input/output)) and (((system adj1 on		
		adj1 chip) or SOC) same (pin or output or		
	69131	input or I/O or input/output))))	HCDAT.	2004/02/22
-	69131	((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or output or	USPAT; US-PGPUB;	2004/03/23
		input or I/O or input/output)	EPO; JPO;	13.33
			DERWENT	
-	74	((binary adj1 decision adj1 diagram) or	USPAT;	2004/03/23
		BDD) and (((system adj1 on adj1 chip) or	US-PGPUB; EPO; JPO;	16:37
		SOC) and (pin or pad or terminal or output or input or I/O or input/output))	DERWENT	
-	33	((core or block or cell or macrocell or	USPAT;	2004/03/24
		cluster) same (pin or pad or terminal or	US-PGPUB;	08:20
		output or input or I/O or input/output))	EPO; JPO;	
		and (((binary adj1 decision adj1 diagram) or BDD) and (((system adj1 on adj1 chip)	DERWENT	
		or SOC) and (pin or pad or terminal or		
		output or input or I/O or input/output)))		
-	1	((compatib\$5 or consisten\$ or agreeing or	USPAT;	2004/03/23
		conforming or accordan\$ or congruous or	US-PGPUB;	16:39
		harmonious or cooperat\$4 or ageeable or	EPO; JPO; DERWENT	
		consonan\$2 or corresponden\$2) with (pin or output or input or I/O or	DEVMENT	
		input/output)) and (((core or block or		
		cell or macrocell or cluster) same (pin		
		or pad or terminal or output or input or		
		<pre>I/O or input/output)) and (((binary adj1 decision adj1 diagram) or BDD) and</pre>		
		(((system adj1 on adj1 chip) or SOC) and		,
		(pin or pad or terminal or output or		
		<pre>input or I/O or input/output))))</pre>		
-	50	(("5663888") or ("5629860") or	USPAT;	2004/03/23
		("5586046") or ("5555188") or ("5541849") or ("5530654") or ("5448497") or	US-PGPUB; EPO; JPO;	16:30
		("5436849") or ("5412591") or ("5349659")	DERWENT	
		or ("5319570") or ("5297053") or		
		("5282140") or ("5220512") or ("5175693")		
		or ("5119314") or ("5068823") or		
		("4868778") or ("4829446") or ("4827428") or ("4701860") or ("4652992") or		
	ı	("4377849") or ("4093990") or		
		("3968478")).PN.		
			·	·

	·		· · · · · · · · · · · · · · · · · · ·	1
-	16	"5910898"	USPAT;	2004/03/23
			US-PGPUB;	16:30
		·	EPO; JPO; DERWENT	
1_	66	((("5663888") or ("5629860") or	USPAT;	2004/03/23
	"	("5586046") or ("5555188") or ("5541849")	US-PGPUB;	16:31
	ļ	or ("5530654") or ("5448497") or	EPO; JPO;	10.31
		("5436849") or ("5412591") or ("5349659")	DERWENT	
		or ("5319570") or ("5297053") or		
	•	("5282140") or ("5220512") or ("5175693")		
	ł	or ("5119314") or ("5068823") or		
		("4868778") or ("4829446") or ("4827428")	ļ	
		or ("4701860") or ("4652992") or		
		("4377849") or ("4093990") or		
		("3968478")).PN.) or "5910898"		
-	1		USPAT;	2004/03/23
		(((("5663888") or ("5629860") or	US-PGPUB;	16:31
		("5586046") or ("5555188") or ("5541849")	EPO; JPO;	
		or ("5530654") or ("5448497") or	DERWENT	
		("5436849") or ("5412591") or ("5349659") or ("5319570") or ("5297053") or		
		("5282140") or ("5220512") or ("5175693")		
		or ("5119314") or ("5068823") or		
		("4868778") or ("4829446") or ("4827428")		
		or ("4701860") or ("4652992") or		
		("4377849") or ("4093990") or		
		("3968478")).PN.) or "5910898")		
-	72840	((system adj1 on adj1 chip) or SOC) and	USPAT;	2004/03/23
	İ	(pin or pad or terminal or node or port	US-PGPUB;	16:37
		or output or input or I/O or	EPO; JPO;	
		input/output)	DERWENT	
-	76	((binary adj1 decision adj1 diagram) or	USPAT;	2004/03/23
		BDD) and (((system adj1 on adj1 chip) or	US-PGPUB;	16:38
		SOC) and (pin or pad or terminal or node	EPO; JPO;	
		or port or output or input or I/O or input/output))	DERWENT	
_	2	((compatib\$5 or consisten\$ or agreeing or	USPAT;	2004/03/23
	_	conforming or accordans or congruous or	US-PGPUB;	16:45
		harmonious or cooperat\$4 or ageeable or	EPO; JPO;	
1		consonan\$2 or corresponden\$2) with (pin	DERWENT	
		or pad or terminal or node or port or		
		output or input or I/O or input/output))		
		and (((binary adj1 decision adj1 diagram)		
		or BDD) and (((system adj1 on adj1 chip)		
		or SOC) and (pin or pad or terminal or		
		node or port or output or input or I/O or		
	27251	input/output)))	USPAT;	2004/02/22
] -	27231	((core or block or cell or macrocell or cluster) same (pin or pad or terminal or	USPAT; US-PGPUB;	2004/03/23 16:44
		node or port or output or input or I/O or	EPO; JPO;	10.44
		input/output)) and (((system adj1 on adj1	DERWENT	
		chip) or SOC) and (pin or pad or terminal	~~····	
		or node or port or output or input or I/O		
		or input/output))		
-	1561	((compatib\$5 or consisten\$ or agreeing or	USPAT;	2004/03/23
		conforming or accordan\$ or congruous or	US-PGPUB;	16:45
		harmonious or cooperat\$4 or ageeable or	EPO; JPO;	
		consonan\$2 or corresponden\$2) with (pin	DERWENT	
		or pad or terminal or node or port or		
		output or input or I/O or input/output))		
]		<pre>and (((core or block or cell or macrocell or cluster) same (pin or pad or terminal</pre>		
		or node or port or output or input or I/O		
		or input/output)) and (((system adj1 on		
		adj1 chip) or SOC) and ((isystem adj1 on		
		terminal or node or port or output or		
		<pre>input or I/O or input/output)))</pre>		

	1	((assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) with (compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23
		and (pin or pad or terminal or node or		
		port or output or input or I/O or		
_	16	input/output)))) ((assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) with (compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node)) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or node or port or output or output or input or I/O or	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:56
	1479	input/output)))) ((core or block or cell or macrocell or cluster) same (pin or pad or terminal or output or input or I/O or input/output)) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((system adjl on adjl chip) or SOC) and (pin or pad or terminal or node or port or output or input or input or I/O or input/output))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:55

	1330	(assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or node or port or output or input or input or I/O or input/output))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:57
-	6925	716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:57
_	40	((assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or node or port or output or input or I/O or input/output))) and 716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23
-	59	(compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 12:36
-	0	((binary adj1 decision adj1 diagram) or BDD) and (((core or block or cell or macrocell or cluster) same (pin or node or port or pad or terminal or output or input or I/O or input/output)) and ((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 08:25
	0	((binary near3 diagram) or (decision near3 diagram) or BDD) and (((core or block or cell or macrocell or cluster) same (pin or node or port or pad or terminal or output or input or I/O or input/output)) and ((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 08:22

				10001/02/01
-	0	((binary near3 diagram) or (decision	USPAT;	2004/03/24 08:25
		near3 diagram) or BDD) and ((compatib\$5 or compliance or consisten\$ or agreeing	US-PGPUB; EPO; JPO;	08:25
		or conforming or accordans or congruous	DERWENT	
		or harmonious or cooperat\$4 or ageeable	DEKWENI	
		or consonan\$2 or corresponden\$2) with pin		
		with (assess\$3 or calculat\$3 or compute		
		or computing or estimat\$3 or levy\$3 or	:	
		reckon\$4 or apprais\$3 or judg\$3))		
_	165	((compatib\$5 or compliance or consisten\$	USPAT;	2004/03/24
	1	or agreeing or conforming or accordan\$ or	US-PGPUB;	09:27
		congruous or harmonious or cooperat\$4 or	EPO; JPO;	
		ageeable or consonan\$2 or corresponden\$2)	DERWENT	
		with pin) same (assess\$3 or calculat\$3 or		
		compute or computing or estimat\$3 or		
		levy\$3 or reckon\$4 or apprais\$3 or		
		judg\$3)		
-	70		USPAT;	2004/03/24
		cluster) same (pin or node or port or pad	US-PGPUB;	12:38
		or terminal or output or input or I/O or	EPO; JPO;	
I		input/output)) and (((compatib\$5 or	DERWENT	
		compliance or consisten\$ or agreeing or		
		conforming or accordan\$ or congruous or		
		harmonious or cooperat\$4 or ageeable or		
		consonan\$2 or corresponden\$2) with pin)		
		same (assess\$3 or calculat\$3 or compute		
		or computing or estimat\$3 or levy\$3 or		
		reckon\$4 or apprais\$3 or judg\$3))		
-	0	((binary adj1 decision adj1 diagram) or	USPAT;	2004/03/24
		BDD) and (((core or block or cell or	US-PGPUB;	08:25
		macrocell or cluster) same (pin or node	EPO; JPO;	
		or port or pad or terminal or output or	DERWENT	
		input or I/O or input/output)) and		
		(((compatib\$5 or compliance or consisten\$		
		or agreeing or conforming or accordan\$ or		
		congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2)		
		with pin) same (assess\$3 or calculat\$3 or		
		compute or computing or estimat\$3 or		
		levy\$3 or reckon\$4 or apprais\$3 or		
		judg\$3)))		
_	2		USPAT;	2004/03/24
	_	near3 diagram) or BDD) and (((core or	US-PGPUB;	08:25
		block or cell or macrocell or cluster)	EPO; JPO;	
	[same (pin or node or port or pad or	DERWENT	
1		terminal or output or input or I/O or		
		input/output)) and (((compatib\$5 or		
	[compliance or consisten\$ or agreeing or		
]	conforming or accordan\$ or congruous or		
		harmonious or cooperat\$4 or ageeable or		
		consonan\$2 or corresponden\$2) with pin)		
		same (assess\$3 or calculat\$3 or compute		
		or computing or estimat\$3 or levy\$3 or		
		reckon\$4 or apprais\$3 or judg\$3)))		
-	26	((core or block or cell or macrocell or	USPAT;	2004/03/24
]	cluster) same (pin or node or port or pad	US-PGPUB;	09:26
		or terminal or output or input or I/O or	EPO; JPO;	
		input/output)) and ((compatib\$5 or	DERWENT	
Ì		compliance or consisten\$ or agreeing or		
		conforming or accordans or congruous or		
		harmonious or cooperat\$4 or ageeable or		
		consonan\$2 or corresponden\$2) with pin		
		with (assess\$3 or calculat\$3 or compute		
		or computing or estimat\$3 or levy\$3 or		
_	55	reckon\$4 or apprais\$3 or judg\$3)) "4928278"	USPAT;	2004/03/24
_	35	4740415	USPAT; US-PGPUB;	09:26
			EPO; JPO;	03.20
			DERWENT	
			~ TILL 1111	i

	T			1 2004 /20 /24
_	1	(((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin) same (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and "4928278"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 09:27
	216	((binary adj1 decision adj1 diagram) or BDD) and ((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24
-	108	((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((binary adj1 decision adj1 diagram) or BDD) and ((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 10:59
_	5		USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 10:57

	5	((on adj2 chip) or SOC) and (((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((binary adj1 decision adj1 diagram) or BDD) and ((compatib\$5 or relat\$ or	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 10:53
	50	compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)))) (("5663888") or ("5629860") or ("5586046") or ("5555188") or ("5541849") or ("5530654") or ("5448497") or ("5339570") or ("5448497") or ("5319570") or ("5297053") or ("5119314") or ("5068823") or ("4868778") or ("4829446") or ("4827428") or ("4701860") or ("4652992") or ("4377849") or ("4093990") or	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 10:52
-	16	("3968478")).PN. "5910898"	USPAT;	2004/03/24
			US-PGPUB; EPO; JPO; DERWENT	10:52
	66	((("5663888") or ("5629860") or ("5586046") or ("5555188") or ("5541849") or ("5586046") or ("5448497") or ("5436849") or ("5412591") or ("5349659") or ("5319570") or ("5297053") or ("5282140") or ("5220512") or ("5175693") or ("5119314") or ("5068823") or ("4868778") or ("4829446") or ("4827428") or ("4701860") or ("4652992") or ("4377849") or ("4093990") or ("3968478")).PN.) or "5910898"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 10:53
	1	((on adj2 chip) or SOC) and (((("5663888") or ("5629860") or ("5586046") or ("5555188") or ("5541849") or ("5530654") or ("5448497") or ("5436849") or ("5412591") or ("5349659") or ("5319570") or ("5297053") or ("5282140") or ("5220512") or ("5175693") or ("5119314") or ("5068823") or ("4868778") or ("4829446") or ("4827428") or ("4701860") or ("4652992") or ("4377849") or ("4093990") or ("3968478")).PN.) or "5910898")	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 10:53
-	385977	((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 13:17

2242	((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output))) ((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or I/O or	USPAT; US-PGPUB; EPO; JPO; DERWENT USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 11:14 2004/03/24 11:18
	((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or	US-PGPUB; EPO; JPO;	,
_			1
5	<pre>input/output)))) ((binary adj1 decision adj1 diagram) or BDD) and (((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 12:42
	apprais\$3 or judg\$3)) and (((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or		
6925	716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 11:01
58	(((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or I/O or input or I/O or input or I/O or or output or input or I/O or input or I/O or input or I/O or input or I/O or Inpu	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 11:03
		BDD) and (((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input/output))))) 58 (((pin or pad or terminal or node or port or output or input/output))))) 716/\$.ccls. 58 (((pin or pad or terminal or node or port or output or input or I/O or input/output)) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or	BDD) and (((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or I/O or input/output) or I/O or input/output) and (core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or I/O or input/output) or I/O or or output or input or I/O or or output or input or I/O or or output or input or I/O or or output or input or I/O or or output or input or I/O or or output or input or I/O or or or output or input or I/O or or or output or input or I/O or or or output or input or I/O or or or output or or or or or or or or or or or or or

Γ_	T 8	boolean and ((((pin or pad or terminal or	USPAT;	2004/03/24
-	·	node or port or output or input or I/O or	US-PGPUB;	12:42
		input/output) with (assess\$3 or	EPO; JPO;	12.72
		calculat\$3 or compute or computing or	DERWENT	
		estimat\$3 or levy\$3 or reckon\$4 or	DEKWENT	
		apprais\$3 or judg\$3)) and (((system adj2		
	1	on adj2 chip) or (on adj2 chip) or SOC)		
	1	and (((compatib\$5 or relat\$ or compliance		
		or consisten\$ or agreeing or conforming		
	İ	or accordan\$ or congruous or harmonious		
		or cooperat\$4 or ageeable or consonan\$2		
		or corresponden\$2) with (pin or pad or		
		terminal or node or port or output or		
		input or I/O or input/output)) and ((core		
		or block or cell or macrocell or unit or		
		cluster) same (pin or pad or terminal or		
		node or port or output or input or I/O or		
	202522	input/output))))) and 716/\$.ccls.)	HCDAM:	2004/03/34
_	202523		USPAT;	2004/03/24
		chip) or on-chip or SOC)	US-PGPUB;	16:03
			EPO; JPO;	
	12150		DERWENT	0004/00/04
-	13152	(((compatib\$5 or relat\$ or compliance or	USPAT;	2004/03/24
		consisten\$ or agreeing or conforming or	US-PGPUB;	11:17
		accordan\$ or congruous or harmonious or	EPO; JPO;	
		cooperat\$4 or ageeable or consonan\$2 or	DERWENT	
		corresponden\$2) with (pin or pad or		
		terminal or node or port or output or		
		input or I/O or input/output)) and ((core		
		or block or cell or macrocell or unit or		
		cluster) same (pin or pad or terminal or		
		node or port or output or input or I/O or		
		<pre>input/output))) and (((system adj1 on</pre>		
		adj1 chip) or (on adj1 chip) or on-chip		
		or SOC))		
-	3880	((pin or pad or terminal or node or port	USPAT;	2004/03/24
		or output or input or I/O or	US-PGPUB;	11:18
		input/output) with (assess\$3 or	EPO; JPO;	
		calculat\$3 or compute or computing or	DERWENT	
		estimat\$3 or levy\$3 or reckon\$4 or		
		apprais\$3 or judg\$3)) and ((((compatib\$5		
		or relat\$ or compliance or consisten\$ or		
		agreeing or conforming or accordan\$ or		
		congruous or harmonious or cooperat\$4 or		
		ageeable or consonan\$2 or corresponden\$2)		
		with (pin or pad or terminal or node or		
		port or output or input or I/O or		
		input/output)) and ((core or block or		
		cell or macrocell or unit or cluster)		
		same (pin or pad or terminal or node or		
		port or output or input or I/O or		
		<pre>input/output))) and (((system adj1 on</pre>		
		adj1 chip) or (on adj1 chip) or on-chip		
		or SOC)))		

_	7	((binary adjl decision adjl diagram) or BDD) and (((pin or pad or terminal or	USPAT; US-PGPUB;	2004/03/24 15:11
		node or port or output or input or I/O or input/output) with (assess\$3 or	EPO; JPO; DERWENT	
		calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or		
		apprais\$3 or judg\$3)) and ((((compatib\$5		
		or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or		•
		congruous or harmonious or cooperat\$4 or		
		ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or		
		port or output or input or I/O or input/output)) and ((core or block or		
		cell or macrocell or unit or cluster)		
		same (pin or pad or terminal or node or port or output or input or I/O or		
		<pre>input/output))) and (((system adj1 on</pre>		
		adj1 chip) or (on adj1 chip) or on-chip or SOC))))		
-	805	(compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or	USPAT; US-PGPUB;	2004/03/24
		congruous or harmonious or cooperat\$4 or	EPO; JPO;	
		ageeable or consonan\$2 or corresponden\$2) with (pin or pad or port or terminal)	DERWENT	
		with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or		
		reckon\$4 or apprais\$3 or judg\$3)		
-	435	((core or block or cell or macrocell or cluster or unit or cluster or subcircuit	USPAT; US-PGPUB;	2004/03/24 13:13
		or subset or sub-circuit or sub-set) same	EPO; JPO; DERWENT	
		<pre>(pin or node or port or pad or terminal)) and ((compatib\$5 or compliance or</pre>	DERWENT	
		consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or		
		cooperat\$4 or ageeable or consonan\$2 or		
		corresponden\$2) with (pin or pad or port or terminal) with (assess\$3 or calculat\$3		
:		or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or		•
		judg\$3))		
_	28	<pre>(((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC)) and (((core or</pre>	USPAT; US-PGPUB;	2004/03/24 12:42
		block or cell or macrocell or cluster or unit or cluster or subcircuit or subset	EPO; JPO; DERWENT	
		or sub-circuit or sub-set) same (pin or	DERWENT	
		node or port or pad or terminal)) and ((compatib\$5 or compliance or consisten\$		
		or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or		
		ageeable or consonan\$2 or corresponden\$2)		
		with (pin or pad or port or terminal) with (assess\$3 or calculat\$3 or compute		
		or computing or estimat\$3 or levy\$3 or		
_	0	reckon\$4 or apprais\$3 or judg\$3))) ((binary adj1 decision adj1 diagram) or	USPAT;	2004/03/24
		BDD) and ((((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC)) and	US-PGPUB; EPO; JPO;	12:42
		(((core or block or cell or macrocell or	DERWENT	
		cluster or unit or cluster or subcircuit or subset or sub-circuit or sub-set) same		
		<pre>(pin or node or port or pad or terminal)) and ((compatib\$5 or compliance or</pre>		
		consisten\$ or agreeing or conforming or		
		accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or		
		<pre>corresponden\$2) with (pin or pad or port or terminal) with (assess\$3 or calculat\$3</pre>		
		or compute or computing or estimat\$3 or		
		levy\$3 or reckon\$4 or apprais\$3 or judg\$3))))		

		, -		
-	13	1 1111 2 2 2 2 1	USPAT;	2004/03/24
		or (on adj1 chip) or on-chip or SOC)) and	US-PGPUB;	12:42
		(((core or block or cell or macrocell or cluster or unit or cluster or subcircuit	EPO; JPO; DERWENT	
	İ	or subset or sub-circuit or sub-set) same	DEKWENI	
		(pin or node or port or pad or terminal))		
		and ((compatib\$5 or compliance or		
		consisten\$ or agreeing or conforming or		
]	accordan\$ or congruous or harmonious or		
		cooperat\$4 or ageeable or consonan\$2 or		
	1	corresponden\$2) with (pin or pad or port		
	}	or terminal) with (assess\$3 or calculat\$3		
		or compute or computing or estimat\$3 or		
		levy\$3 or reckon\$4 or apprais\$3 or		
		judg\$3))))		
-	1434579	1 , ,	USPAT;	2004/03/25
		cluster or unit or subcircuit or subset	US-PGPUB;	10:05
		or sub-circuit or sub-set) with	EPO; JPO;	
		(interfac\$3 or connect\$3 or	DERWENT	
		interconnect\$3 or (output and input) or		
	264407	I/O or input/orput))	HCDAM -	2004/02/24
_	364487	((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or agreeable or	USPAT;	2004/03/24
	1	"same" or similar) with (pin or pad or	US-PGPUB; EPO; JPO;	13:22
		terminal or node or port or output or	DERWENT	
		input or I/O or input/output)) and	DERWENT	
		(((core or block or cell or macrocell or		
ļ.		cluster or unit or subcircuit or subset		
		or sub-circuit or sub-set) with		
		(interfac\$3 or connect\$3 or		
		interconnect\$3 or (output and input) or		
		I/O or input/orput)))		
-	16734	\L L L	USPAT;	2004/03/24
		(assess\$3 or calculat\$3 or compute or	US-PGPUB;	15:10
		computing or estimat\$3 or levy\$3 or	EPO; JPO;	
		reckon\$4 or apprais\$3 or judg\$3) and	DERWENT	•
		(((compatib\$5 or relat\$ or compliance or		
		consisten\$ or agreeing or agreeable or "same" or similar) with (pin or pad or		
1		terminal or node or port or output or		
		input or I/O or input/output)) and		
		(((core or block or cell or macrocell or		
		cluster or unit or subcircuit or subset		
		or sub-circuit or sub-set) with		
		(interfac\$3 or connect\$3 or		
		interconnect\$3 or (output and input) or		
		I/O or input/orput))))		
-	1428	((pin or pad or port or terminal) with	USPAT;	2004/03/24
		(assess\$3 or calculat\$3 or compute or	US-PGPUB;	15:10
		computing or estimat\$3 or levy\$3 or	EPO; JPO;	
[reckon\$4 or apprais\$3 or judg\$3) and	DERWENT	
		<pre>(((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or agreeable or</pre>		
		"same" or similar) with (pin or pad or		
		terminal or node or port or output or		
		input or I/O or input/output)) and		
		(((core or block or cell or macrocell or		
		cluster or unit or subcircuit or subset		
j l		or sub-circuit or sub-set) with		
		(interfac\$3 or connect\$3 or		
		interconnect\$3 or (output and input) or		
		I/O or input/orput))))) and (((system		
		adjl on adjl chip) or (on adjl chip) or		
i l		on-chip or SOC))		

-	0	((binary adj1 decision adj1 diagram) or	USPAT;	2004/03/24
1		BDD) and (((pin or pad or port or	US-PGPUB;	15:11
		terminal) with (assess\$3 or calculat\$3 or	EPO; JPO;	
		compute or computing or estimat\$3 or	DERWENT	ŀ
		levy\$3 or reckon\$4 or apprais\$3 or		
		<pre>judg\$3) and (((compatib\$5 or relat\$ or</pre>		
		compliance or consisten\$ or agreeing or		
		agreeable or "same" or similar) with (pin]
		or pad or terminal or node or port or		
		output or input or I/O or input/output))		
		and (((core or block or cell or macrocell or cluster or unit or subcircuit or	ļ	
		subset or sub-circuit or sub-set) with		
		(interfac\$3 or connect\$3 or		
		interconnect\$3 or (output and input) or		
		I/O or input/orput))))) and (((system		
		adj1 on adj1 chip) or (on adj1 chip) or		
		on-chip or SOC)))		
_	125	boolean and (((pin or pad or port or	USPAT;	2004/03/24
		terminal) with (assess\$3 or calculat\$3 or	US-PGPUB;	15:17
	1	compute or computing or estimat\$3 or	EPO; JPO;	
		levy\$3 or reckon\$4 or apprais\$3 or	DERWENT	
	1	judg\$3) and (((compatib\$5 or relat\$ or		
		compliance or consisten\$ or agreeing or		
1	1	agreeable or "same" or similar) with (pin		
	1	or pad or terminal or node or port or		
	1	output or input or I/O or input/output))		
	1	and (((core or block or cell or macrocell		
		or cluster or unit or subcircuit or		
		subset or sub-circuit or sub-set) with		
		(interfac\$3 or connect\$3 or		
		interconnect\$3 or (output and input) or		
		I/O or input/orput))))) and (((system		
		adjl on adjl chip) or (on adjl chip) or		
		on-chip or SOC)))		
-	32	,	USPAT;	2004/03/24
		pad or port or terminal) with (assess\$3	US-PGPUB;	15:58
		or calculat\$3 or compute or computing or	EPO; JPO;	
		estimat\$3 or levy\$3 or reckon\$4 or	DERWENT	
		apprais\$3 or judg\$3) and (((compatib\$5 or relat\$ or compliance or consisten\$ or		
		agreeing or agreeable or "same" or		
		similar) with (pin or pad or terminal or		
		node or port or output or input or I/O or		
		input/output)) and (((core or block or		
		cell or macrocell or cluster or unit or		
		subcircuit or subset or sub-circuit or		
		sub-set) with (interfac\$3 or connect\$3 or		
		interconnect\$3 or (output and input) or		
		I/O or input/orput)))) and (((system		
		adj1 on adj1 chip) or (on adj1 chip) or		
		on-chip or SOC))))		
-	18	match\$3 and ((interconnect\$3 adj4 core)	USPAT;	2004/03/24
		same pin)	US-PGPUB;	15:58
]			EPO; JPO;	
			DERWENT	
-	96	(interconnect\$3 adj4 core) same pin	USPAT;	2004/03/25
			US-PGPUB;	09:01
			EPO; JPO;	
]			DERWENT	0004/02/04
-	2		USPAT;	2004/03/24
		core) same pin)	US-PGPUB;	16:00
			EPO; JPO;	
]_	25	consists4 and //interconnects2 adia come	DERWENT	2004/03/24
	25	consist\$4 and ((interconnect\$3 adj4 core) same pin)	USPAT;	2004/03/24
		same bill	US-PGPUB; EPO; JPO;	16:05
			DERWENT	
_	10	((system adj1 on adj1 chip) or (on adj1	USPAT;	2004/03/24
]	10	chip) or on-chip or SOC) and	US-PGPUB;	16:04
		((interconnect\$3 adj4 core) same pin)	EPO; JPO;	
		(,pin	DERWENT	
	L			

-	3	(consist\$4 or compliance) and (((system	USPAT;	2004/03/24
		adj1 on adj1 chip) or (on adj1 chip) or	US-PGPUB;	16:09
		on-chip or SOC) and ((interconnect\$3 adj4	EPO; JPO;	
		core) same pin))	DERWENT	
-	3	(match\$3 and ((interconnect\$3 adj4 core)	USPAT;	2004/03/24
i		<pre>same pin)) and ((consist\$4 or compliance)</pre>	US-PGPUB;	16:10
1		and (((system adjl on adjl chip) or (on	EPO; JPO;	
		adj1 chip) or on-chip or SOC) and	DERWENT]
ľ		((interconnect\$3 adj4 core) same pin)))		<u> </u>